SIMULATION OF Si AND GaAs SUBMICRON TRAPATT DIODES

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Plasma formation and extraction processes in submicron silicon N⁺NP⁺, GaAs N⁺NP⁺, and GaAs NM Schottky TRAPATT (TRApped Plasma Avalanche Triggered Transit) diodes were simulated. The simulation of GaAs TRAPATT diodes was done for the first time. The quasi-hydrodynamic model was chosen for the simulation of the processes. The Synopsys TCAD Sentaurus Software Package was used. The carrier mobility dependence on phonon scattering, impurity scattering, carrier-carrier scattering, intervalley scattering (for GaAs), and mobility saturation in a high electric field was taken into account. Several generation-recombination mechanisms were included: impact ionization, Shockley–Read–Hall recombination (SRH) with doping-dependent lifetimes, trap-assisted tunnelling, band-to-band tunnelling, Auger recombination, and radiative recombination (for GaAs). We show that the so-called critical current density for plasma formation in the submicron diodes increases with the active layer thickness decrease and almost does not depend on the doping density in the active layer. The critical current density for silicon diodes is about two times lower than for the GaAs diodes. The intensive minority carrier storage in the N⁺ and P⁺ regions has a high influence on the voltage oscillation amplitude and frequency after the first plasma formation and extraction period. Oscillation damping takes place in the N⁺NP⁺ GaAs diode with the active layer thickness of less than 0.3 μ m.

Keywords: simulation, avalanche diodes, mobility, minority carrier storage

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1. Introduction

TRAPATT diodes were invented in [1] as high-power microwave devices working with high efficiency. The best achieved result on efficiency (75 percent) was reported in [2]. Peak power output of 1.2 kW at 1.9 GHz with 24% efficiency from five series-connected diodes has been obtained in [3]. Very important results on powerful TRAPATT diodes were reported in [4]. The physical mechanism of the new avalanche diode operation mode was described in [5] by computer simulation. The analytical model of the TRAPATT diode was proposed in [6,7]. It was shown that, contrary to the transit time mode, the main role in achieving high efficiency is played by the space charge of the carriers generated during the short but extremely intensive avalanche process in the whole active region of the diode. If the total current density is sufficiently high, the space charge density of free carriers exceeds the density of the ionized impurity several times and defines the electric field shape in the active region. The generated electrons and holes move in opposite directions. This causes the electric field to drop to a very low value during several picoseconds. As a result, we have electron-hole-ionized impurity plasma in the active region of the diode. The plasma extraction period (up to several hundred picoseconds) in a low electric field follows the plasma formation process. Due to a relatively long plasma extraction period, the frequency of the TRAPATT generator is several times lower than in the transit time mode. The maximum frequency of the TRAPATT generator obtained experimentally is in the X-band [8, 9]. TRAPATT diodes are suitable for phased-array radar systems [10]. In [11–13] it was shown that TRAPATT diodes are useful not only for generating sinusoidal oscillation, but for generating triangular pulses with sub-nanosecond duration as well. Optical synchronization, amplification, frequency, and amplitude modulation were demonstrated experimentally [14–16].

Due to the above-mentioned advantages TRA-PATT diodes have been useful for microwave power generation up to now. But these diodes have several disadvantages: start-up jitter when using TRAPATT diodes for microwave pulse generation in modern radar systems, relatively high current density, and relatively low frequency (less than 10 GHz). The start-up jitter may be substantially reduced by proper design of the external circuit of the TRAPATT generator and by using P⁺PN⁺ or P-type Schottky diodes [17]. The generation frequency increase may be achieved by using silicon or GaAs TRAPATT diodes with a submicron active layer. We carefully inspected the literature on TRAPATT diodes and have not found any papers with experimental or theoretical investigation of silicon submicron TRAPATT diodes. We also have not found any papers on GaAs TRAPATT diodes.

The main processes in the TRAPATT diode are: electron-hole plasma formation due to very intensive impact ionization in a high electric field and plasma extraction in a low electric field. The plasma formation process and the resulting plasma density depend on the N⁺N interface steepness [18, 19], on the electron diffusion coefficient in the high electric field [19], and on the dynamic minority carrier storage [20, 21]. Because of the dynamic minority carrier storage, the initial conditions for the first and the second oscillation period are not the same. We have aperiodic oscillation which may be the reason of oscillation jitter in the real external circuit of the TRAPATT generator. In [20] the dynamic minority carrier storage was obtained for the first time in the N⁺NP⁺ diode. In [21] a comparison between N⁺NP⁺ and P⁺PN⁺ diodes was made, but with the assumption that the carrier diffusion coefficient is independent of the electric field and the doping density. Thus, we can consider these results as qualitative results, because the carrier diffusion coefficient dependence on the electric field strongly affects the plasma formation process in the TRAPATT diode [19]. The detailed simulation of various silicon TRAPATT diodes with a 2–3 μ m active layer was made in [17]. The most periodic oscillation was obtained in the P-type Schottky diodes without a P⁺ laver.

In all above-mentioned calculations, carrier-carrier scattering, Shockley–Read–Hall recombination with doping dependent lifetimes, trap-assisted tunnelling, Auger recombination, band-to-band tunnelling, and radiative recombination were not taken into account.

The main purpose of our work is computer simulation of submicron TRAPATT diodes for the generation of oscillation with the frequency above 10 GHz and a comparison of silicon and GaAs TRAPATT diodes.

2. TRAPATT diode model

The doping distribution in the modelled TRAPATT diodes is shown in Fig. 1. Donor $N_d(x)$ and acceptor $N_a(x)$ distribution is approximated by a well-known function erfc(x) [19], where x is distance, donor and acceptor density at the contacts $N_{d0} = 5 \cdot 10^{18}$ cm⁻³ and $N_{a0} = 10^{19}$ cm⁻³, donor density in the active lay-

er $N_{d1} = 2.4 \cdot 10^{15}$ cm⁻³ and 10¹⁶ cm⁻³, and total length of the N⁺NP⁺ diodes w = 0.6 and 0.7 μ m. The N⁺N junction steepness described by the parameter $l_d = 0.01 \ \mu$ m. The NP⁺ junction steepness described by the parameter $l_a = 0.01$ and 0.05 μ m. We also investigated a simplified Schottky diode without a N⁺ region. This diode consists of only an active N region and a Schottky barrier on the right side of it. The total length of the simplified diode $w = 0.2 \ \mu$ m. Because of the diode simplicity it is not shown in Fig. 1.

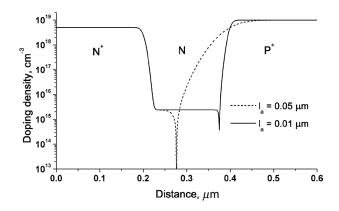


Fig. 1. Doping distribution of simulated N^+NP^+ TRAPATT diodes.

For computer simulation of the TRAPATT diodes we used Boltzmann statistics at lattice temperature T = 300 K and a one-dimension quasi-hydrodynamic model based on Poisson's equation for electrostatic potential, continuity equations for electrons and holes, and energy balance equations for electrons and holes according to *Synopsys* TCAD Sentaurus:

$$\nabla \cdot \varepsilon \nabla \phi = -q(p-n+N_{d}(x)-N_{a}(x)), \qquad (2.1)$$

$$q\frac{\partial n}{\partial t} - \nabla \cdot J_{\rm n} = qG_{\rm ii} - qR_{\rm net} , \qquad (2.2)$$

$$q\frac{\partial p}{\partial t} + \nabla \cdot J_{\rm p} = qG_{\rm ii} - qR_{\rm net} , \qquad (2.3)$$

$$\frac{\partial W_{n}}{\partial t} + \nabla \cdot S_{n} = J_{n} \cdot \nabla E_{C} - \xi_{n} \frac{W_{n} - W_{n0}}{\tau_{en}} , \qquad (2.4)$$

$$\frac{\partial W_{\rm p}}{\partial t} + \nabla \cdot S_{\rm p} = J_{\rm p} \cdot \nabla E_{\rm V} - \xi_{\rm p} \frac{W_{\rm p} - W_{\rm p0}}{\tau_{\rm ep}}, \qquad (2.5)$$

where *t* is time, *n* is electron density, *p* is hole density, ϕ is electrostatic potential, J_n , J_p are the electron and hole current densities, G_{ii} is the charge generation

term due to impact ionization, R_{net} is the net recombination rate, q is the electronic charge, ε is electrical permittivity, W_{n} , W_{p} are the electron and hole energy densities, S_n , S_p are the electron and hole energy fluxes, $E_{\rm c}$, $E_{\rm v}$ are conduction and valence band edges, W_{n0} , W_{n0} are the energy densities at $T_n = T_n = T$, T_n , $T_{\rm p}$ are the electron and hole temperatures, the energy relaxation times for electrons and holes $\tau_{en} = 0.3$ ps, $\tau_{\rm ep} = 0.25$ ps for silicon and $\tau_{\rm en} = 1$ ps, $\tau_{\rm ep} = 0.4$ ps for GaAs. The parameters $\xi_{\rm n}$, $\xi_{\rm p}$ improve numeric stability. They speed up relaxation for small densities and they approach 1 for large densities:

$$\xi_{\rm n} = 1 + \frac{n_{\rm min}}{n} \left(\frac{n_0}{n_{\rm min}}\right)^{\rm max\left[0, (T-T_{\rm n})/100\,K\right]}$$
(2.6)

and likewise for ξ_{p} . Here, n_{\min} and n_{0} are adjustable small density parameters.

$$J_{\rm n} = \mu_{\rm n} \left(n \nabla E_{\rm C} + k T_{\rm n} \, \nabla n \right), \tag{2.7}$$

$$J_{\rm p} = \mu_{\rm p} \left(p \nabla E_{\rm V} + k T_{\rm p} \nabla p \right), \tag{2.8}$$

where $\mu_{n} \mu_{p}$ are the electron and hole mobility, k is Boltzmann constant.

The net recombination rate

$$R_{\rm net} = R_{\rm net}^{\rm SRH} + R_{\rm net}^{\rm A} + R_{\rm net}^{\rm BB} + R_{\rm net}^{\rm R}, \qquad (2.9)$$

where R_{net}^{SRH} is Shockley-Read-Hall recombination rate with doping dependent [22] and field dependent (Schenk trap-assisted tunnelling model [23]) lifetimes for electrons and holes, R_{net}^A is the band-to-band Auger recombination rate with temperature dependent Auger coefficients for silicon [24–26], R_{net}^{BB} is the band-to-band tunnelling rate [27], R_{net}^{R} is the radiative recombination rate (for GaAs). The doping-dependent bandgap narrowing E_{ben} was calculated using the Slotboom model [28–31].

The energy densities are given by:

$$W_{\rm n} = n \left(\frac{3kT_{\rm n}}{2}\right),\tag{2.10}$$

$$W_{\rm p} = p \left(\frac{3kT_{\rm p}}{2}\right). \tag{2.11}$$

The energy fluxes are:

$$S_{n} = -\frac{5r_{n}\lambda_{n}}{2} \left(\frac{kT_{n}}{q}J_{n} + f_{n}^{hf}\hat{\kappa}_{n}\nabla T_{n}\right), \qquad (2.12)$$

$$S_{\rm p} = -\frac{5r_{\rm p}\lambda_{\rm p}}{2} \left(\frac{-kT_{\rm p}}{q} J_{\rm p} + f_{\rm p}^{\rm hf} \hat{\kappa}_{\rm p} \nabla T_{\rm p} \right), \qquad (2.13)$$

$$\hat{\kappa}_{n} = \frac{k^2}{q} n \mu_{n} T_{n} , \qquad (2.14)$$

$$\hat{\kappa}_{\rm p} = \frac{k^2}{q} p \mu_{\rm p} T_{\rm p} , \qquad (2.15)$$

where $\lambda_n = \lambda_p = 1$, $r_n = r_p = 0.6$, $f_n^{hf} = f_p^{hf} = 1$. The generation rate due to impact ionization can be expressed as

$$G_{ii} = \alpha_n n \nu_n = \alpha_p p \nu_p, \qquad (2.16)$$

where α_{n} , α_{n} are the ionization coefficients for electrons and holes, $v_{\rm p}$, $v_{\rm p}$ are the electron and hole drift velocities. For calculation of the ionization coefficients we used the van Overstraeten and de Man model [32] based on the Chynoweth law:

$$\alpha \left(F^{\text{eff}} \right) = a \exp \left(-\frac{b}{F^{\text{eff}}} \right).$$
(2.17)

The effective field F_n^{eff} for electrons was calculated using the expression

$$nv_{\text{sat, n}}F_{n}^{\text{eff}} = \frac{n3k(T_{n} - T)}{2q\lambda_{n}\tau_{\text{en}}} + \frac{\gamma_{n}}{q} \left(E_{\text{g}} + \delta_{n}kT_{n}\right) \alpha_{n}nv_{\text{sat, n}}, \qquad (2.18)$$

where $v_{sat n}$ is the electron saturation velocity. A similar equation is used to determine F_{p}^{eff} . The parameters $\gamma_n = \gamma_p = 1$ for silicon and equal to zero for GaAs, $\delta_{n} = \delta_{p} = 3/2.$

The carrier mobility dependence on phonon scattering, impurity scattering, carrier-carrier scattering, intervalley scattering (for GaAs), and mobility saturation in a high electric field was taken into account. The electron and hole low field mobility in the undoped semiconductor due to phonon scattering $\mu_n = 1417 \text{ cm}^2/\text{Vs}$, $\mu_p = 470.5 \text{ cm}^2/\text{Vs}$ for silicon and $\mu_{\rm p} = 8500 \text{ cm}^2/\text{Vs}, \mu_{\rm p} = 400 \text{ cm}^2/\text{Vs}$ for GaAs. The doping-dependent mobility proposed by Masetti et al. [33] was used for silicon. The doping-dependent mobility for GaAs was calculated using the Arora model [34]. The carrier-carrier scattering model is based on Choo [35] and Fletcher [36]. This model was used for silicon.

The mobility saturation in a high electric field for electrons and holes in silicon and for holes in GaAs describes the Canali model [37].

For GaAs a negative differential mobility can be observed for high driving fields. TCAD Sentaurus includes a transferred electron model for the description of this effect, as given by [38].

The contacts on semiconductors are ohmic. The charge neutrality and equilibrium are assumed at Ohmic contacts:

$$n_0 - p_0 = N_d - N_a, \qquad (2.19)$$

$$n_0 p_0 = n_{i,\text{eff}}^2,$$
 (2.20)

where n_0 , p_0 are the electron and hole equilibrium concentrations, $n_{i,eff}$ is the effective intrinsic density (including doping-dependent bandgap narrowing).

At the Schottky contact the following boundary conditions hold:

$$\phi = \phi_{\rm F} - \Phi_{\rm B} + \frac{kT}{q} \ln\left(\frac{N_{\rm C}}{n_{\rm i,eff}}\right), \qquad (2.21)$$

$$J_{\rm n} = q v_{\rm n} (n - n_0^{\rm B}), \qquad (2.22)$$

$$J_{\rm p} = q v_{\rm p} (p - p_0^{\rm B}), \qquad (2.23)$$

$$n_0^{\rm B} = N_{\rm C} \exp\left(\frac{-q\Phi_{\rm B}}{kT}\right), \qquad (2.24)$$

$$p_0^{\rm B} = N_{\rm v} \exp\left(\frac{-E_{\rm g,eff} + q\Phi_{\rm B}}{kT}\right), \qquad (2.25)$$

where $\phi_{\rm F}$ is the Fermi potential at the contact which equals the applied voltage, $N_{\rm C}$, $N_{\rm V}$ are the density of states in conduction and valence band, $E_{\rm g,eff} = E_{\rm g} - E_{\rm bgn}$, $E_{\rm g}$ is the bandgap, $\Phi_{\rm B}$ is the barrier height (the difference between the contact workfunction and the electron affinity of the semiconductor), $v_{\rm n} = 2.573 \cdot 10^6$ cm/s, $v_{\rm p} = 1.93 \cdot 10^6$ cm/s are the thermionic emission velocities. The barrier-lowering model for Schottky contact was taken into account.

For the carrier temperatures T_n and T_p , at the thermal contacts, fast relaxation to the lattice temperature (boundary condition $T_n = T_p = T$) is assumed.

Almost all calculations were made assuming a constant total current density. An external circuit containing capacitances or other elements was not used. The boundary conditions for the electric field F (the derivative of the electrostatic potential) were calculated by solving the equation for the total current density:

$$J(t) = \varepsilon \frac{\partial F}{\partial t} + J_{\rm n} + J_{\rm p} \,. \tag{2.26}$$

The set of the above-mentioned nonlinear equations derived in difference form was solved using the Newton method.

3. The computer simulation results

The typical voltage oscillation on the silicon N⁺NP⁺ TRAPATT diode (total current density J = 74.7 kA/ cm²) is shown in Fig. 2. The carrier density in the depletion region at the beginning of the first oscillation period is very low, about 10³-10⁴ cm⁻³. Thus, almost the whole current is a displacement current and we have the electric field increasing at a constant rate. As we can see in Fig. 2, the diode voltage increases linearly until very intensive impact ionization begins. The carrier density increases rapidly. Near the voltage maximum point the carrier density becomes much larger than the donor density in the active layer, and according to Poisson's equation the carrier density defines the electric field shape. In the active layer the electric field decreases with time. The diode voltage decreases as well. In the time interval of about several picoseconds the diode voltage decreases to a very low value. We have a very high carrier density (above 10^{16} cm⁻³) in a low electric field (600–700 V/ cm). Almost in the whole active region $n - p = N_{d_1}$, which indicates electron-hole-ionized donor plasma. The next process is the plasma extraction in the low field. After this process (15-20 ps) the diode voltage increases again and the next oscillation period begins. The oscillation period depends on the plasma extraction time. It depends on the carrier density in plasma and the applied total current density. Larger current density leads to a more rapid increase of diode voltage, a higher voltage maximum, and a more intensive impact ionization process. As a result we have a denser electron-hole plasma and a prolonged plasma

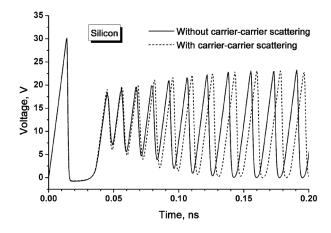


Fig. 2. Voltage oscillation on the silicon N⁺NP⁺ TRAPATT diode with $w_a = 0.3 \mu m$ at total current density $J = 74.7 \text{ kA/cm}^2$.

extraction process. Thus, the first oscillation period increases with the total current density increase. All next oscillation periods differ from the first one sufficiently (Fig. 2) due to a very high total current density needed for diodes with a submicron active layer. The difference between the first and second voltage oscillation periods is much greater than in the diodes with the active layer of several microns [17]. The high current density leads to a high voltage increasing rate and a very short increasing time after plasma extraction. The minority carriers accumulated in N⁺ and P⁺ regions need more time for extraction. They support a residual carrier density in the active region that is several orders higher than at the beginning of the first oscillation period. As a result, we have a very early impact ionization process, a low voltage maximum, a low carrier density in the plasma, and a very short second oscillation period. The oscillation amplitude increases with time. This transient process continues for about 100 ps.

In Fig. 2 we can see the effect of carrier-carrier scattering on voltage oscillation of a silicon TRAPATT diode with the active layer thickness of 0.3 μ m. The carrier-carrier scattering does not change the first oscillation period. The oscillation amplitude differs slightly during the transient process. The frequency and amplitude of the steady state oscillation does not depend on carrier-carrier scattering.

The total current density must exceed the critical current density J_{cr} for plasma formation in the diode. The simple analytical theory of the TRAPATT diode [6, 7] shows that

$$J_{\rm cr} = q N_{\rm d1} v_{\rm sat}, \qquad (3.27)$$

where N_{d1} is the donor density in the active layer, v_{sat} is the electron saturation velocity in a high electric field. Equation (3.27) is based on the assumption that the impact ionization wave propagates from the NP⁺ interface to the N⁺ region during the plasma formation period. When $J > J_{cr}$, the wave propagation velocity $v_{\rm w} > v_{\rm sat}$ and the carriers accumulate in the active layer filling it with electron-hole-ionized donor plasma. This theory works very well for relatively long diodes. In the diodes with the active layer thickness w_{a} of about 3 μ m we have the electric field collapse in the centre of the diode propagating to the left and to the right at the same time [17]. The critical current density in these diodes is about two times less than we can obtain from Eq. (3.27). $J_{\rm cr} = q N_{\rm d1} v_{\rm sat}$ for silicon diodes with the active layer of about 1 μ m and for GaAs diodes with the active layer of about 1.2 μ m (Fig. 3). For the active layer thickness of less than 0.5 μ m, the critical current density increases dramatically (Fig. 3). $J_{cr} = 45 \text{ kA/cm}^2$

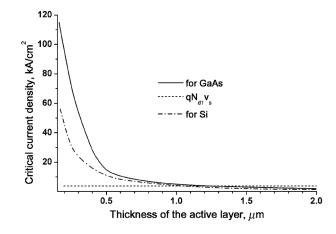


Fig. 3. Critical current density for silicon and GaAs N⁺NP⁺ TRAPATT diodes.

for the silicon diode, and $J_{cr} = 95 \text{ kA/cm}^2$ for the GaAs diode with $w_{1} = 0.2 \ \mu m$. This effect can be explained as follows. The dynamic avalanche breakdown voltage $U_{\rm dyn}$ necessary for plasma formation is several times greater than the static breakdown voltage U_{st} . U_{dvn} may be achieved only when the voltage increasing rate is sufficiently high. This rate is inversely proportional to the depletion layer capacitance and directly proportional to the total current density which is almost displacement current density before intensive impact ionization begins. The depletion layer capacity of the diodes with $w_1 > 1 \,\mu\text{m}$ is relatively low and the critical current density depends mainly on $N_{\rm d1}$ according to Eq. (3.27). For the diodes with $w_a < 1 \ \mu m$ the depletion layer capacitance is sufficient and J_{cr} is inversely proportional to the active layer thickness and does not depend on the doping density in the active layer. The diode voltage oscillation remains almost the same when changing the doping density from $2.4 \cdot 10^{15}$ cm⁻³ to 10^{16} cm⁻³ (Fig. 4). The voltage oscillation in Figs. 2 and 4 shows that

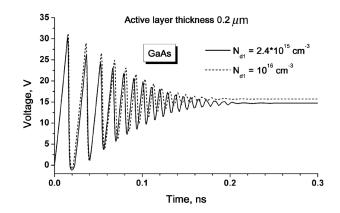


Fig. 4. Voltage oscillation on the N⁺NP⁺ GaAs TRAPATT diode with the various doping density in the active layer. Total current density $J = 112 \text{ kA/cm}^2$.

oscillation frequency of up to 50 GHz may be achieved using silicon and GaAs TRAPATT diodes. The main disadvantage of these diodes is an extremely high current density which restricts the practical application of TRAPATT diodes with $w_a < 0.2 \ \mu m$. The critical current density for silicon diodes is about two times lower than for the GaAs diodes. This fact may be explained as follows. The bandgap of GaAs is about 1.5 times wider than of silicon. So, the dynamic breakdown voltage is higher. The electrical permittivity of GaAs is higher than that of silicon and as a consequence the depletion layer capacitance is higher. As a result, we need higher current density for reaching U_{dyn} at the same time as in silicon diodes. Thus, silicon diodes are more appropriate for practical application than GaAs ones.

In Fig. 5 we can see the trap-assisted tunnelling effect on voltage oscillation of a GaAs TRAPATT diode. The oscillation amplitude is lower and the frequency is higher when the trap-assisted tunnelling is taken into account. The tunnelling begins at a relatively low electric field of about 3.10⁵ V/cm. The positive values of

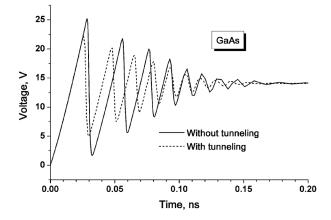


Fig. 5. Voltage oscillation on the N⁺NP⁺ GaAs TRAPATT diode with $w_a = 0.2 \ \mu m$ and $l_a = 0.05 \ \mu m$ with and without trap-assisted tunnelling. Total current density $J = 56 \text{ kA/cm}^2$.

 R_{net}^{SRH} in Fig. 6 mean the carrier recombination rate and the negative values mean the carrier generation rate. At the beginning of the first oscillation period (t = 14 ps, Fig. 6) R_{net}^{SRH} with tunnelling is several orders greater than R_{net}^{SRH} without tunnelling and the avalanche generation rate. As a result, the carrier density in the active layer at the beginning of the oscillation period is about three orders higher (Fig. 7). Due to this carrier density, avalanche generation begins earlier and the voltage maximum becomes lower resulting in a lower carrier density in plasma and a more rapid plasma extraction. The trap-assisted tunnelling effect on voltage

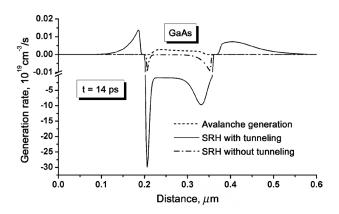


Fig. 6. Avalanche generation and SRH generation-recombination rate in the N⁺NP⁺ GaAs TRAPATT diode with $w_a = 0.2 \,\mu\text{m}$ and $l_a = 0.05 \,\mu\text{m}$ with and without trapassisted tunnelling. Total current density $J = 56 \text{ kA/cm}^2$.

oscillation of silicon diodes is inferior to that on GaAs diodes, because the bandgap in silicon is narrower than in GaAs. The impact ionization in silicon begins at a lower electric field and the R_{net}^{SRH} comprises a lower part in the overall charge generation rate.

The band-to-band tunnelling takes place at the electric field higher than 8·10⁵ V/cm. At such high electric field the avalanche generation rate is about six to seven orders greater than the generation rate due to the band-to-band tunnelling. Thus, the effect is negligible and we cannot see any difference in voltage oscillation with and without band-to-band tunnelling.

The Auger and radiative recombination rate is negligible in comparison with the R_{net}^{SRH} . Thus, the Auger and radiative recombination may be neglected.

The main difference between N⁺NP⁺ silicon and GaAs TRAPATT diodes is the voltage oscillation damping on GaAs diodes with the active layer thickness of less than 0.3 μ m (Fig. 8). This effect may be

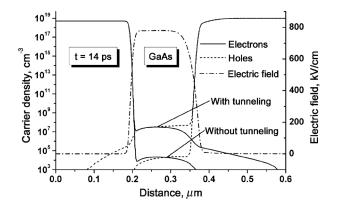


Fig. 7. Carrier and electric field distribution in the N⁺NP⁺ GaAs TRAPATT diode with $w_a = 0.2 \ \mu m$ and $l_a = 0.05 \ \mu m$ with and without trap-assisted tunnelling. Total current density $J = 56 \text{ kA/cm}^2$.

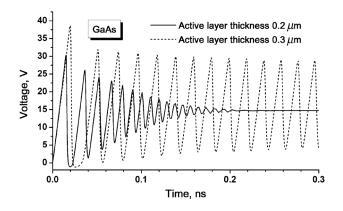


Fig. 8. Voltage oscillation on the N⁺NP⁺ GaAs TRAPATT diode with $w_a = 0.2 \ \mu m \ (J = 112 \ kA/cm^2)$ and $w_a = 0.3 \ \mu m \ (J = 74.7 \ kA/cm^2)$.

explained as follows. The initial carrier density in the active layer at the beginning of the first and second voltage oscillation periods is different mainly due to the minority carrier accumulation in the N⁺ and P⁺ layers. The thickness of these layers is the same in all simulated diodes. Thus, the accumulated charge is almost the same as well (depends on the total current density). After the plasma formation period the electrons and holes move from the P⁺ and N⁺ regions to the active region. So, the initial carrier density at the beginning of the second oscillation period is by several orders higher than at the beginning of the first period. The smaller the active region, the higher the density of the initial carriers. In the diode with the active layer of 0.2 μ m the initial carrier density increases from one period to the next (Fig. 9) which causes the voltage oscillation amplitude to decrease with time (Fig. 8). In silicon diodes with the same active layer

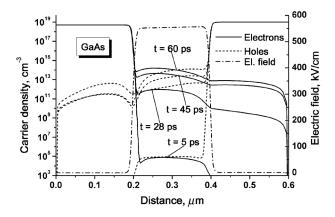


Fig. 9. Carrier density and electric field distribution in the N⁺NP⁺ GaAs TRAPATT diode with $w_a = 0.2 \ \mu m$ at the beginning of the first four oscillation periods. Total current density $J = 112 \text{ kA/cm}^2$.

thickness the initial carrier density decreases from one period to the next and we have the voltage oscillation amplitude increasing with time. The carrier extraction from the active layer in silicon diodes is more rapid than in GaAs diodes due to higher electron velocity in the electric field of about 10⁵ V/cm.

The voltage oscillation damping effect is sufficiently lower in the diodes without N⁺ and P⁺ regions. We calculated a GaAs N-type Schottky diode without the N⁺ region and with the active layer thickness of 0.2 μ m. Some oscillation damping takes place during the transient period of about 100 ps. After that period we have steady state oscillation with the constant amplitude and the frequency exceeding 50 GHz (Fig. 10). Thus, the N-type GaAs Schottky diode is more appropriate than the N⁺NP⁺ diode for the generation of microwave power with the frequency above 50 GHz.

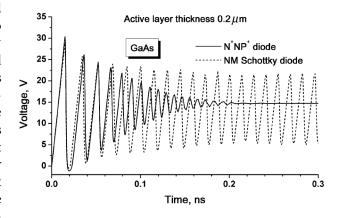


Fig. 10. Voltage oscillation on the N⁺NP⁺ and NM Schottky GaAs TRAPATT diode with $w_a = 0.2 \mu m$. Total current density $J = 112 \text{ kA/cm}^2$.

All our calculations made at a constant total current density show the maximum oscillation frequency which may be achieved by the TRAPATT generator. Any real TRAPATT generator consists of a TRAPATT diode and an external circuit. In such a circuit total current does not remain constant during the oscillation period. In the Kawamoto circuit [11] the shape of the current pulses is near to the triangular [13, 16] and does not depend on the oscillation frequency [13, 16]. The duration of the pause between the current pulses decreases with frequency and is equal to zero at the maximum frequency. The pause between current pulses is very important for the residual carrier extraction from the active layer after the plasma formation period. The oscillation damping in the GaAs TRAPATT diode may be overcome by using a pulsed total current with the appropriate pause between the pulses. In Fig. 11 we can see a quite periodic

Fig. 11. Current density and voltage oscillation on the N⁺NP⁺ GaAs TRAPATT diode with $w_a = 0.2 \ \mu m$. The amplitude of the total current density pulses $J_A = 112 \text{ kA/} \text{ cm}^2$.

oscillation in the N⁺NP⁺ GaAs TRAPATT diode with the active layer of 0.2 μ m. The period of the current pulses is equal to 35 ps. Decreasing the period leads to aperiodic oscillation due to high residual carrier density in the active layer at the beginning of the next oscillation period. The residual carrier density decreases with time during the pause between current pulses (Fig. 12). At t = 35 ps we have the same carrier density in the active layer as at the beginning of the previous oscillation period. This means that the periodic oscillation at the frequency of 28.6 GHz is the maximum frequency for the N⁺NP⁺ GaAs TRAPATT diode with the active layer of 0.2 μ m. A much higher frequency (of about 50 GHz) may be achieved in diodes with the active layer of 0.3 μ m (Fig. 8). Thus, the production of the N⁺NP⁺ GaAs TRAPATT diode with the active layer of less than 0.3 μ m does not make sense.

10¹ 600 10¹³ Electrons GaAs 500 ······ Holes kV/cm ື່ພ 10¹⁶ - El. field t = 22 ps 400 10¹³ = 28 p Carrier density, field, 10¹ 300 ectric 10[°] 200 10 100 10^t t = 35 ps 0 10³ 0.3 0.4 0.1 0.2 0.5 0.6 0.0 Distance, µm

Fig. 12. Carrier density and electric field distribution in the N⁺NP⁺ GaAs TRAPATT diode with $w_a = 0.2 \ \mu m$ during the pause between the first and second current density pulses.

4. Conclusions

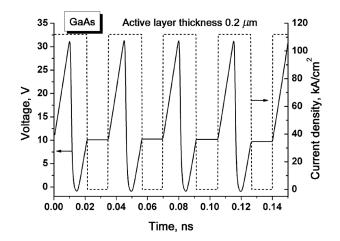
Plasma formation and extraction processes in submicron silicon and GaAs N+NP+ and GaAs NM Schottky TRAPATT diodes were simulated. The critical current density for plasma formation in such diodes is inversely proportional to the active layer thickness and almost does not depend on the donor density in the active layer. The critical current density in the silicon diodes is about two times less than in the GaAs diodes. The extremely high current density restricts the practical application of TRAPATT diodes with $w_2 < 0.2 \ \mu m$. The carrier-carrier scattering, Auger recombination and band-to-band tunnelling does not have a sufficient effect on the voltage oscillation at the constant current density. However, the trap-assisted tunnelling must be taken into account when modelling TRAPATT diodes with a submicron active layer. The effect is smaller in silicon TRAPATT diodes due to the narrower bandgap in silicon. The main difference between N⁺NP⁺ silicon and GaAs TRAPATT diodes is the voltage oscillation damping on GaAs diodes with the active layer thickness of less than 0.3 μ m. The periodic oscillation with the maximum frequency of about 30 GHz in these diodes may be achieved only in the case of pulsed total current with an appropriate duration of the pause between current pulses. Steady state oscillation with the frequency exceeding 50 GHz can be obtained in N-type GaAs Schottky diodes without a N⁺ region and with the active layer of 0.2 μ m.

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Si IR GaAs SUBMIKRONINIŲ TRAPATT DIODŲ MODELIAVIMAS

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Santrauka

Darbe pateikti plazmos susidarymo ir išsiurbimo procesų submikroniniuose silicio N⁺NP⁺, GaAs N⁺NP⁺ ir GaAs NM Šotki TRAPATT (TRApped Plasma Avalanche Triggered Transit) dioduose modeliavimo rezultatai. GaAs TRAPATT diodai modeliuoti pirmą kartą. Procesų modeliavimui pasirinktas kvazi-hidrodinaminis modelis. Naudotas *Synopsys* TCAD Sentaurus programų paketas. Įvertinta krūvininkų judrio priklausomybė nuo sklaidos fononais, priemaišinės sklaidos, krūvininkų-krūvininkų sklaidos, tarpslėninės sklaidos galio arsenide, taip pat įvertintas judrio įsisotinimas stipriame elektriniame lauke. Įskaityta keletas generacijos-rekombinacijos mechanizmų: smūginė jonizacija, Šoklio-Rido-Holo rekombinacija (SRH) su priklausančia nuo priemaišų krūvininkų gyvavimo trukme, tuneliavimas per gaudykles, tuneliavimas zona-zona, Ožė rekombinacija, spinduliuojamoji rekombinacija galio arsenide. Parodyta, kad plazmos formavimosi krizinės srovės tankis submikroniniuose dioduose didėja mažinant aktyviojo sluoksnio storį ir beveik nepriklauso nuo priemaišų tankio tame sluoksnyje. Silicio diodų krizinės srovės tankis apie du kartus mažesnis negu galio arsenido diodų. Intensyvus šalutinių krūvininkų kaupimas N⁺ ir P⁺ srityse stipriai veikia įtampos virpesių amplitudę bei dažnį po pirmojo plazmos susidarymo ir išsiurbimo periodo. Virpesiai slopsta N⁺NP⁺ GaAs dioduose su aktyviosios srities storiu, mažesniu nei 0,3 μ m.