ANALYSIS OF p-n JUNCTION BOUNDARY CONDITIONS AT HIGH-LEVEL INJECTION OF MINORITY CARRIERS

A. Baškys

Faculty of Electronics, Vilnius Gediminas Technical University, Naugarduko 41, LT-03227 Vilnius, Lithuania E-mail: algirdas.baskys@vgtu.lt

Received 18 November 2013; accepted 4 December 2013

The equations of boundary conditions of the junction, which can be an alternative to the commonly used exponential equations, are proposed. The derived equations take into account the main event that arises at the high-level injection – dependence of majority carrier boundary concentrations on voltage drop across the depletion region that is essential for the p-n junctions of the present-day semiconductor devices, but is not reflected in the exponential equations of boundary conditions.

Keywords: p-n junction, boundary conditions, high-level injection, analytic model, electronic circuits simulation **PACS:** 85.30.De, 85.30.Kk

1. Introduction

The p-n junction is still the basic building block of many semiconductor devices. Because of this, the equation of voltage–current (v-i) characteristic of the p-n junction depletion region remains important in creating analytic models for simulation of electronic circuits and understanding the operation of semiconductor devices. The basis for derivation of the equation of the depletion region v-i characteristic are the boundary conditions which present the equations that relate the boundary concentrations of minority carriers at the edges of the depletion region and the voltage drop across the depletion region (U_d) and is central to the p-n junction theory. The present-day analytic models of the semiconductor devices used for simulation of electronic circuits [1-3] and for educational purposes [4-6] are based on the exponential equation of the depletion region v-i characteristic obtained using the exponential equations of boundary conditions of the junction [7]. However, there is a well known contradiction between the theory that offers exponential equations of boundary conditions of the p-n junction and the present-day reality. The problem is that current density of junctions, especially of those that are used in power semiconductor devices and in high-speed integrated circuits, has increased strongly since the exponential equations of boundary conditions of the junction were introduced and, as a consequence, the validity of some assumptions used during the derivation of these equations is violated in recent semiconductor devices. It is obvious that it is impossible to obtain analytic equations of boundary conditions of the junction in explicit form, which would take into account most events caused by the high current density; however, some improvement of exponential equations is possible.

The only difference of derivation procedure of equations of boundary conditions of the junction proposed in this work as compared to the classical procedure is that derivation is carried out considering the dependence of the boundary concentrations of majority carriers on U_d , which is essential at increased current density. This improvement allows us to obtain more general equations of boundary conditions of the junction as compared to the exponential equations. The derivation is carried out on the basis of the universally accepted Boltzmann relations and charge quasi-neutrality conditions that are acceptable at relatively high current density [3, 8, 9]. The equations are obtained assuming that the junction is one-dimensional, abrupt, and homogeneously doped.

2. Derivation of equations of boundary conditions of the junction

Using Boltzmann relations

$$p_{n}(x_{n}) = p_{p}(-x_{p}) \exp\left(-\frac{V_{B}-U_{d}}{V_{T}}\right) ,$$

$$n_{p}(-x_{p}) = n_{n}(x_{n}) \exp\left(-\frac{V_{B}-U_{d}}{V_{T}}\right) ,$$
(1)

and charge quasi-neutrality conditions

$$p_{\rm p}(-x_{\rm p}) = N_{\rm A} + n_{\rm p} (-x_{\rm p}),$$

$$n_{\rm n}(x_{\rm n}) = N_{\rm D} + p_{\rm n} (x_{\rm n}),$$
 (2)

the following system of equations can be derived:

$$\begin{cases} p_{n}(x_{n}) = n_{p}(-x_{p}) \exp\left(-\frac{V_{B}-U_{d}}{V_{T}}\right) - N_{D} \\ n_{p}(-x_{p}) = p_{n}(x_{n}) \exp\left(\frac{V_{B}-U_{d}}{V_{T}}\right) - N_{A} \end{cases}$$
(3)

and solved in terms of $p_n(x_n)$ and $n_p(-x_p)$:

$$p_{n}(x_{n}) = \frac{N_{A} \exp\left(\frac{V_{B} - U_{d}}{V_{T}}\right) + N_{D}}{\exp\left(2\frac{V_{B} - U_{d}}{V_{T}}\right) - 1} ,$$

$$n_{p}(-x_{p}) = \frac{N_{D} \exp\left(\frac{V_{B} - U_{d}}{V_{T}}\right) + N_{A}}{\exp\left(2\frac{V_{B} - U_{d}}{V_{T}}\right) - 1} ,$$
(4)

where $p_n(x_n)$, $n_p(-x_p)$ are hole and electron (minority carrier) boundary concentrations and $n_n(x_n)$, $p_p(-x_p)$ are electron and hole (majority carrier) boundary concentrations at the edges of the depletion region $x = x_n$, and $x = -x_p$ at given voltage U_d , N_A and N_D are impurity (acceptor and donor) concentrations, V_T is the thermal voltage, and V_B is the potential barrier of the junction.

It should be noted that the p-n junction is typically analysed assuming that the whole applied voltage appears across the depletion region. Because of this, in most books dedicated to physics of semiconductor devices the voltage U_d in the equations of boundary conditions of the junction is called the applied voltage.

Using Eqs. (1) at $U_d = 0$ and taking into account the fact that for actual junctions $\exp[2(V_B/V_T)] >> 1$ and $\exp(V_B/V_T) >> N_A/N_D$, N_D/N_A , the impurity concentrations N_A and N_D can be expressed in terms of minority carrier concentrations at the equilibrium state ($U_d = 0$) n_{p0} and p_{n0} . Substituting these expressions into Eqs. (4), the following equations of boundary concentrations of minority carriers are obtained:

$$p_{n}(x_{n}) = p_{n0} \exp\left(\frac{U_{d}}{V_{T}}\right) \frac{1 + \frac{n_{p0}}{p_{n0}} \exp\left(-\frac{V_{B} - U_{d}}{V_{T}}\right)}{1 - \exp\left(-2\frac{V_{B} - U_{d}}{V_{T}}\right)} ,$$

$$n_{p}(-x_{p}) = n_{p0} \exp\left(\frac{U_{d}}{V_{T}}\right) \frac{1 + \frac{p_{n0}}{n_{p0}} \exp\left(-\frac{V_{B} - U_{d}}{V_{T}}\right)}{1 - \exp\left(-2\frac{V_{B} - U_{d}}{V_{T}}\right)} .$$
(5)

Equations (5) are the equations of boundary conditions of the junction considering the dependence of the boundary concentrations of majority carriers on U_d . The only difference of the derivation procedure of Eqs. (5) as compared to the classical procedure, which leads to the exponential equations, is that simplifying assumptions $p_p(-x_p) = N_A$ and $n_n(x_n) = N_D$ used in the classical procedure were rejected, i. e. the means that allow us to analyse how the dependence of $p_p(-x_p)$ and $n_n(x_n)$ on U_d affects the boundary conditions of the junction were not removed. It is necessary to stress that the validity of derived equations of boundary conditions of the junction (5) is limited by the validity of the Boltzmann relations (1) and charge quasi-neutrality conditions (2).

If the junction is symmetric, i. e. $n_{p0} = p_{n0}$, Eqs. (5) simplify to

$$p_{n}(x_{n}) = p_{n0} \frac{\exp\left(\frac{U_{d}}{V_{T}}\right)}{1 - \exp\left(-\frac{V_{B} - U_{d}}{V_{T}}\right)} ,$$

$$n_{p}(-x_{p}) = n_{p0} \frac{\exp\left(\frac{U_{d}}{V_{T}}\right)}{1 - \exp\left(-\frac{V_{B} - U_{d}}{V_{T}}\right)} .$$
(6)

It is seen that the exponential equations of boundary conditions of the junction is a specific case of (5) and (6) at low U_d (at $U_d < V_B - several V_T$). For example, Eqs. (6) coincide with the exponential equations when $\exp[-(V_B - U_d)/V_T] < 1$. Assuming that this condition is valid when $\exp[-(V_B - U_d)/V_T] < 0.01$, the U_d can be considered low for the symmetric junction if it meets the requirement $U_d < V_B - 4.6V_T$. In case of the asymmetric junction, the threshold value of U_d , starting from which U_d can be considered low, is different for holes and electrons (5) and is dependent on the ratio of impurity concentrations because $n_{p0}/p_{n0} = N_D/N_A$.

Equations (5) take into account the occurrences of the depletion region that emerge at high $U_{\rm d}$ (at $V_{\rm B}$ – *several* $V_{\rm T} < U_{\rm d} < V_{\rm B}$) and are not reflected in the equations of the exponential boundary conditions of the junction. These occurrences are:

(i) the increased steepness of $p_n(x_n)$ and $n_p(-x_p)$ dependences on U_d as compared to the exponential boundary conditions;

(ii) the limitation of the U_{d} value by the V_{B} value;

(iii) the decrease of the asymmetry of the asymmetric p-n junction.

These occurrences, which are not widely discussed in the literature dedicated to semiconductor devices, can be explained by the fact that at high U_d the boundary concentrations of majority carriers $p_p(-x_p)$ and $n_n(x_n)$ become dependent on U_d . Because of this, the injection is provided from the regions with the variable concentration of majority carriers. Since $p_p(-x_p)$ and $n_n(x_n)$ increase when U_d increases, the law of the dependence becomes more steep as compared to the exponential law. The appreciable change of boundary concentrations of majority carriers in the lower doped region starts at lower U_d ; therefore, first of all the law of the dependence fails to agree with the exponential law for the minority carriers injected into the higher doped region when U_d increases.

The concentration of minority carriers injected into the lower doped side is higher as compared to that injected into the higher doped side. Because of this, the rise of U_d lowers the asymmetry of the junction and, what is more, at high U_d the $p_p(-x_p)$ and $n_n(x_n)$ in the asymmetric junction may become comparable.

The proposed equations of boundary conditions of the junction (5) were used for the derivation of equations of the p-n junction carrier injection level [10]. The equation of v-i characteristic of the p-n junction depletion region obtained on the basis of (5) was employed for analytic analysis of integrated circuits based on the differential amplifiers [11, 12].

It is obvious that any analytic model is an approximation which takes into account more or fewer facts of a real device. The derived equations of boundary conditions of the junction (5) and (6) include occurrences that are essential for the p-n junctions of the present-day semiconductor devices. However, they do not take into consideration such effects as dependence of $V_{\rm B}$ on $U_{\rm d}$ or electron-hole scattering that come into play at high U_{d} . The reason is that inclusion of these effects eliminates the possibility to obtain the equations of boundary conditions of the junction in explicit form. On the other hand, the exponential equations of the boundary conditions of the junction do not include any factor that arises at high U_d , but in spite of this they are the basis of present-day analytic electric models of semiconductor devices used for simulation of electric circuits and are the only equations of the boundary conditions of the junction used for training the p-n junction operation.

The main purpose of this work was to derive the equations that could be an alternative to the exponential equations of boundary conditions of the junction. Therefore, it is of interest to discuss and analyse them from this point of view.

3. Analysis of concrete p-n junctions

To examine the derived equations of boundary conditions of the junction, an asymmetric ($N_A = 10^{17} \text{ cm}^{-3}$, $N_{\rm D} = 10^{15} {\rm cm}^{-3}$) silicon p-n junction was analysed. The analysis was performed at room temperature assuming that junctions are abrupt and homogeneously doped. Using widely known relations $V_{\rm B} = V_{\rm T} \ln[N_{\rm A}N_{\rm D}/n_{\rm i}^2]$, $p_{\rm n0} = n_{\rm i}^2/N_{\rm D}$ and $n_{\rm p0} = n_{\rm i}^2/N_{\rm A}$ ($n_{\rm i}$ is intrinsic carrier concentration), the dependences of $p_{\rm n}(x_{\rm n})$ and $n_{\rm p}(-x_{\rm p})$ on $U_{\rm d}$ on the basis of the exponential equations and Eqs. (5) were computed (Fig.1).

It is seen (Fig. 1) that at low U_d the dependences calculated using exponential equations and Eqs. (5) coincide. When U_d becomes high and increases, Eqs. (5) give a higher rise of minority carrier concentrations as compared to the exponential law. The values of $p_n(x_n)$ and $n_p(-x_p)$ calculated using (5) fail to agree with the result obtained using exponential equations at different U_d (Fig. 1). This disagreement starts at lower U_d for $n_p(-x_p)$, i. e. for the minority carriers injected into the higher doped region.

Furthermore, the dependences obtained on the basis of Eqs. (5) show that in the asymmetric p-n junction, when U_d is close to V_B , the values of $p_n(x_n)$ and $n_p(-x_p)$ may become comparable (solid lines in figure).

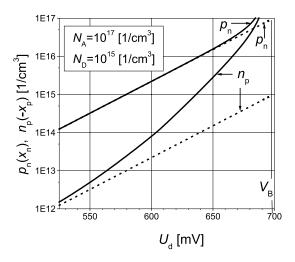


Fig. 1. Dependences of minority carrier boundary concentrations on the voltage drop across the depletion region for asymmetric silicon junction calculated using equations of boundary conditions of the junction (5) (solid lines) and exponential equations (dashed lines).

4. Conclusions

1. The derived equations of boundary conditions of the junction are an alternative to the commonly used exponential equations.

2. The obtained Eqs. (5) take into account the following occurrences that emerge at high U_d and are not reflected in the equations of the exponential boundary conditions of the junction:

(i) the increased steepness of $p_n(x_n)$ and $n_p(-x_p)$ dependences on U_d as compared to the exponential law;

(ii) the limitation of the U_{d} value by the V_{B} value;

(iii) the decrease of the asymmetry of the asymmetric p-n junction.

3. The validity of derived equations of boundary conditions of the junction is limited by the validity of the Boltzmann relations and charge quasi-neutrality conditions.

Acknowledgements

This work was supported partly by the Agency for Science, Innovation and Technology (MITA) under project 31V-37 (2012), 31V-35 (2013).

References

- M. Schroter, S. Lehmann, S. Fregonese, and T. Zimmer, A computationally efficient physics-based compact bipolar transistor model for circuit design part I: model formulation, IEEE Trans. Electron Dev. 53(2), 279–286 (2006).
- [2] S. Fregonese, S. Lehmann, T. Zimmer, M. Schroter, D. Celi, B. Ardouin, H. Beckrich, P. Brenner, and W. Kraus, A computationally efficient physicsbased compact bipolar transistor model for circuit design – part II: parameter extraction and experimental results, IEEE Trans. Electron Dev. 53(2), 287–295 (2006).

- [3] G. Massobrio and P. Antognetti, Semiconductor Device Modeling with SPICE (McGraw-Hill, New York, 1993).
- [4] R.F. Pierret, Semiconductor Device Fundamentals (Addison-Wesley Publishing Company, New York, 1996).
- [5] M.N. Horenstein, *Microelectronic Circuits and Devices* (Prentice-Hall, Englewood Cliffs, 1996).
- [6] S. Štaras, Fundamentals of Electronics: Semiconductor Device Physics (Technika, Vilnius, 1999).
- [7] H.R. Pota, A new derivation of the law of the junctions, IEEE Trans. Educ. **47**(4), 497–499 (2004).
- [8] T.T. Mnatskanov, D. Schroder, and A.E. Schlogl, Effect of high injection level phenomena on the feasibility of diffusive approximation in semiconductor device modeling, Solid State Electron. 42(1), 153–163 (1998).
- [9] R.S. Muller and T.I. Kamins, *Device Electronics for Integrated Circuits* (John Wiley and Sons, New York, 1986).
- [10] A. Baskys, M. Sapurov, and R. Zubavicius, The new equations of p-n junction carrier injection level, Elektronika ir elektrotechnika (Electronics and Electrical Engineering) 19(2), 45–48 (2013).
- [11] A. Baskys, L. Nickelson, and R. Navickas, Analytical method for differential amplifier offset voltage analysis, Elektronika ir elektrotechnika (Electronics and Electrical Engineering) 108(2), 51–54 (2011).
- [12] A. Baskys, R. Navickas, and C. Simkevicius, The fast differential amplifier-based integrated circuit yield analysis technique, Acta Phys. Pol. A 119(2), 259–261 (2011).

p-n SANDŪROS KRAŠTINIŲ SĄLYGŲ ANALIZĖ DIDELĖS ŠALUTINIŲ KRŪVININKŲ INJEKCIJOS REŽIME

A. Baškys

Vilniaus Gedimino technikos universitetas, Elektronikos fakultetas, Vilnius, Lietuva

Santrauka

Pasiūlytos p-n sandūros ribinių sąlygų išraiškos, kurios yra alternatyva plačiai naudojamoms eksponentinėms išraiškoms. Įvertintas pagrindinis reiškinys, pasireiškiantis esant aukštai šalutinių krūvininkų injekcijai – pagrindinių krūvininkų tankio priklausomybė nuo įtampos kritimo nuskurdintame sluoksnyje. Tai aktualu šiuolaikinių puslaidininkinių prietaisų sandūroms, tačiau nėra įvertinama eksponentinėse sandūros ribinių sąlygų išraiškose. Pasiūlytos išraiškos gali būti pritaikytos kuriant naujus analitinius puslaidininkinių prietaisų elektrinius modelius elektroninių schemų modeliavimui.